10/751,171 & Interference Scarch!

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
520 7	413173	LCD or liquid adj crystal adj display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 10:50
S20 8	55	S207 and (data adj driver or signal adj driver) with (memory and register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:10
\$20 9	0	S208 and register and (requency near3 enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR _	OFF	2006/07/28 11:02
521 . 0	0	S208 and register and (requency with enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:02
S21 1	2	S208 and register and (frequency with enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:22
S21 2	466	S207 and (data adj driver or signal adj driver) with (register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:10
S21 3	0	S212 and register with (frequency near3 enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:12
S21 4	0	S212 and register same (frequency near3 enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:12

7/29/06 9:54:33 AM

S21 5	0	S212 and register with (frequency with enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:12
S21 6	4	S212 and register same (frequency with enable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:23
S21 7	3	S216 not S211	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:12
S21 8	3	S208 and register and (frequency with control adj signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:23
S21 9	1	S218 not S211	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:23
S22 0	5	S212 and register same (frequency with control adj signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR.	OFF	2006/07/28 11:26
S22 1	12	S212 and register same (frequency with control)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:24
S22 2	4	S220 not S216	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:24

S22 3	9	S221 not S216	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2006/07/28 11:24
S22 4	2	S220 and frequency near3 control	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:27
S22 5	4	S222 and frequency with control	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:32
S22 6	0	S225 not S220	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:33
S22 7	5	S223 not S220	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:38
S22 8	2037132	display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:39
S22 9	10350	S228 and (data or signal) adj driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:39
S23 0	0	S228 and (((data or signal) adj driver) near (register with stor\$4 with frequency) near3 (control))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:42

S23 1	24873	S228 and register with bit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:42
S23 2	119	S228 and (data or signal) adj driver same (register with bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:42
S23 3	68	S228 and (data or signal) adj driver with (register with bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:43
S23 4	13	S233 and bit near3 (chang\$4 or determin\$4 or switch\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:44
S23 5	2	S233 and (bit near3 (chang\$4 or determin\$4 or switch\$4) near3 frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:45
S23 6	1	S233 and bit near3 frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:46
S23 7	6	S233 not (shift adj register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/28 11:46

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	7	(US-20050168252-\$ or US-20050140621-\$ or US-20040239668-\$).did. or (US-4844590-\$ or US-6232946-\$ or US-5856818-\$ or US-6909414-\$).did.	US-PGPUB; USPAT	OR	OFF	2006/07/29 08:55
L10	6	9 and (frequency and (control or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2006/07/29 09:03
L11	0	9 and register not (shift adj register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:04
L12	7	9 and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:05
L13	6	12 and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:05
L14	6	12 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:06
L15	4	12 and cycle	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:06
L16	20131	((register or memory or bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:07

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L17	2185151	(lcd or liquid adj crystal or display) ((register or memory or bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:07
L18	5331	(lcd or liquid adj crystal or display) and ((register or memory or bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:08
L19	3232	(lcd or liquid adj crystal or display) and ((register or bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:08
L20	4685	((lcd or liquid adj crystal or display) and (bit or byte) same frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:09
L21	1908	((lcd or liquid adj crystal or display) and (bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:10
L22	3	((lcd or liquid adj crystal or display) and (data or signal) adj driver with (bit or byte) with frequency near3 (clock or enable))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:14
L23	22	data adj driver with (register near3 (bit or byte))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2006/07/29 09:28
L24	9	23 and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:28

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L25	94	data adj driver same data adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:45
L26	73	data adj driver with data adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:45
L27	57	26 and display	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:45
L28	25	27 and frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:46
L29	16	28 and enable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/29 09:51
L30	1	((lcd or liquid adj crystal adj display) and pixels and data adj driver and memory and register and signal adj controller and bit and frequency and data adj enable adj signal).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/29 09:53